

REMARKS

This supplemental amendment corrects grammatical errors in the fifth paragraph and the fourteenth paragraph of the remarks section of the Amendment dated May 5, 2006.

Previously in the fifth paragraph, Applicant stated "Further, Morishima does not disclose a termination load as required in claims 8 and 0." In the replacement paragraph below, Applicant has corrected the sentence to read "Further, Morishima does not disclose a termination load as required in claims 8 and 9."

"Therefore, Morishima certainly does not disclose a device for generating a difference signal on the basis of an input signal and a delayed signal as required by claims 8 and 9. In addition, Morishima does not disclose the possibility of decoding a bit by means of the different signal as required in claim 15. **Further, Morishima does not disclose a termination load as required in claims 8 and 9.** The Examiner is of the opinion that Morishima describes in column 10, lines 20-24 the provision of such a termination load. It is respectfully submitted that this is not correct as this portion of Morishima only specifies that the delay circuit 7 may have more inverters connected in series to another of resistors, capacitors, and the like to increase the delay time. Thus, the elements mentioned at the give portion in Morishima are not termination loads. Rather, these elements can only be considered as part of the delay line."

Previously in the fourteenth paragraph, Applicant stated "However, Zhang teaches a differential amplifier receiving two input signals for generating a difference signal as defined in claims 16 and 17." In the replacement paragraph below, Applicant has corrected the sentence to read "However, Zhang does not teach a differential amplifier receiving two input signals for generating a difference signal as defined in claims 16 and 17."

"Claims 16 and 17 were further rejected under 35 U.S.C. 102(b) as being anticipated by Zhang, *et al.* Applicant also respectfully disagrees that Zhang even makes obvious, much less teaches the invention described in claim 16 and 17. Zhang discloses a delay generator as shown in FIG. 1, which outputs a plurality of delayed signals 110, 120. One of these signals is selected by multiplexer 170 (see column 3, lines 14 to 17). However, Zhang does not teach a differential amplifier receiving two input signals for generating a difference signal as defined in claims 16 and 17. Further, nothing is derivable from Zhang with regard to selecting one of a plurality of first input signals and selecting one of a plurality of special input signals depending on the duration of the positive part and the negative part of the bipolar input pulse."

No new matter has been added by this supplemental amendment. If the Examiner should have any questions or feel that a discussion would advance the prosecution. Applicant invites the Examiner to contact Applicant's attorney at the telephone number listed below. No fee is believed due in connection with this filing. However, in the event that there are any fees due, please charge the same, or credit any overpayment, to Deposit Account No. 50-1065.

Respectfully submitted,

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Date

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